



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

500.39521X00

Applicants: ISHII et al.

Serial No.: 09/764,487

Filed: January 18, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND DATA PROCESSOR DEVICE

Group: 2824

Examiner: A. Tran

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RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner
for Patents
Washington, D.C. 20231

October 29, 2002

Sir:

In response to the restriction requirement dated October 3, 2002, applicants hereby elect the Group I, claims 1-7 drawn to a semiconductor memory device, without traverse.

If the Examiner believes that there are any other points which may be clarified or otherwise disposed of, either by telephone discussion or by personal interview, the Examiner is invited to contact applicants' undersigned attorney at the number indicated below.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 500.39521X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By

Gregory E. Montone
Registration No. 28,141

GEM/kd
703/312-6600